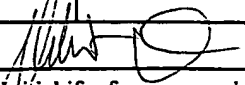


U.S. Department of Commerce, Patent and Trademark Office				Atty Docket No.		Serial No.		
				M-15338 US		Not yet assigned		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Applicant(s)				
(Use several sheets if necessary)				Nui Chong; Aaron Rogers; Kerry Ilgenstein				
				Filing Date		Group		
				Herewith		Not yet assigned		
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
RTP	AA	6,028,758	02/22/2000	Sharpe-Geisler				
RTP	AB	6,091,595	07/18/2000	Sharpe-Geisler				
	AC							
	AD							
	AE							
	AF							
	AG							
	AH							
	AI							
	AJ							
	AK							
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
RTP	AQ	ESD Protection For Mixed-Voltage I/O Using NMOS Transistors Stacked In A Cascode Configuration, by Warren R. Anderson et al., Digital Equipment Corporation, Shrewsbury, MA 01545						
RTP	AR	U.S. patent application serial number 10/664,170, filed on 09/16/2003, entitled "Electrostatic Discharge Protection"						
RTP	AS	U.S. patent application serial number 10/769,174, filed on 01/29/2004, entitled "Electrostatic Discharge Simulation"						
Examiner				Date Considered		4-12-06		
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.								